



* When the output voltage is 1.0 kV or greater

Hipot (Withstanding Voltage) & Insulation Resistance Tester

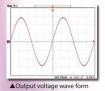
TOS5300 Series

The PWM amp system provides highly-stable output 5 kV/100 mA (500 VA) AC Hipot (withstanding voltage) test 6 kV/maximum output 50 W DC Hipot (withstanding voltage) tester (TOS5301) 25 V-1000 V (7 steps), 500 V or greater, up to 5.00 GΩ Insulation Resistance test High-precision measurement ±1.5 % of reading (with voltmeter 500 V or higher, Ammeter 1 mA or higher) Rise time/Fall time control Key lock function and Protection cover for key operation Equipped with USB interface



Highly stable

Newly developed, high-efficiency PWM switching amplifier!



Providing a stable output of high voltage without being affected by AC line variation. Ensure the user to perform highly reliable testing with confidence, even in regions with large voltage variations. (Input voltage fluctuation rate: ±0.3%)

Rise/Fall Time control function of the applied voltage

Equipped with a Rise time/ Fall time control function

Prevents from an excessive stress applied to the EUT or for standard tests.

Pursuing usability

and safety

All new design of the control

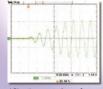
panel and output terminals!

Eliminates the projected components of output terminals, and equips with a new type of the LOW

terminal. Pursuing the improvement of safety

and a convenience in production line, such as







Supporting the **World-wide** input voltage

Reducing

the tact time

Increasing the productivity!

Capable of setting the test time from 0.1s

Usable globally!

Usable in any country without changing the input power supply.

Selectable output frequency!

The instrument not rely on the input power environment. Supplying the stable test voltage with 50/60 Hz frequencies.

A new standard for 🕹 (egastov gnibnasatsiW) રંભ્ભોને Insulation resistance testing Applied to World-Wide

Lightweight & **Compact design**

Increasing your work efficiency!

Weighs Approx. 15 kg 40% lighter than conventional models *Compared to TOS5300 and TOS8870A

input voltage



Right : LOW (black, with lock function)



▲View with the protection coveremoved from front panel

Short-circuit current: 200 mAor more!* Max rated output: 500 VA



When the output voltage is 1.0 kV or greater

Hipot (Withstand-Voltage) & Insulation Resistance Tester

The "TOS5300 Series" is a series of test instruments used in Hipot (withstanding voltage) tests and insulation resistance tests, two of the four tests regarded as necessary for ensuring the safety of electrical products. With an output of 5 kV/100 mA (AC) and 6 kV/10 mA (DC), the series can be used in Hipot (withstanding voltage) & insulation resistance testing of electronic equipment and electronic parts, based on the requirements of IEC, EN, UL, VDE, JIS, and other international safety standards and the Electrical Appliance and Material Safety Law. Also, the test voltage stability is improved with the adoption of a newly developed switching amplifier. Since the output voltage can be kept constant even when the AC line voltage or frequency changes, consistent testing can be performed, even when the power supply environment is in an unstable region. The TOS5300 is also equipped with a number of features that are capable of meeting a variety of test needs. It is a new low-cost standard model that provides thorough operability, reliability and safety.

Applied test / Model	TOS5300	TOS5301	TOS5302
AC Hipot (Withstanding voltage) test (ACW)	V	V	V
DC Hipot (Withstanding voltage) test (DCW)		V	
Insulation Resistance test (IR)			V

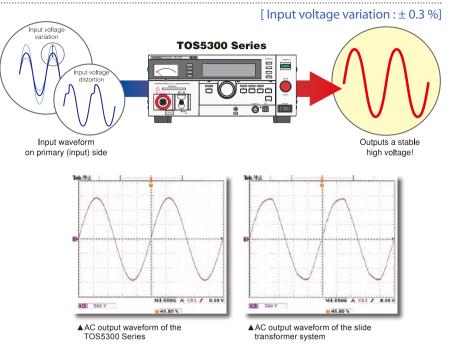
Features and **Functions**

●ACW: 5 kV/100 mA; DCW: 6 kV/50 W ●IR: 25-1000 V (7 steps)/500 V or greater, up to 5.00 GΩ ●High-precision measurement ±1.5 % of reading (Voltage: 500 V or greater; Current: 1 mA or more) ●Rise time / Fall time control function ●Discharge function ●World-wide input voltage ●AUTO function ●USB interface ●Panel memory function (3 sets) ●Key lock and Protection cover for panel operation

The achievement of AC Hipot (Withstanding voltage) testing with a constant stable output!

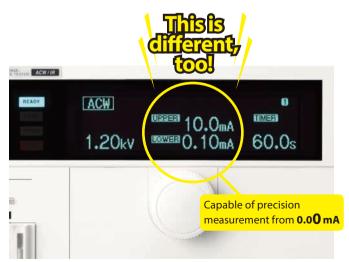
The output waveform is essential factor in Hipot (Withstanding voltage) testing!

A conventional Hipot (Withstanding voltage) tester boosts and outputs the AC line's input voltage through the use of a slide transformer. With this slide transformer system, input voltage fluctuations will affect the output, preventing tests from being performed properly. At times, the application of distortion voltage applied to the EUT may cause a failure of new product (accelerating a deterioration of components). Since the TOS5300 Series equips with a highefficient PWM amplifier that can output a stable high-voltage without being affected by the variation of AC power line, users can perform "safe", "stable", and highly "reliable" tests with confidence, even in regions with large voltage variations.



Realizing high-precision measurement with high-resolution and high-speed judgement

Equipped with a high-accuracy, high-resolution of True RMS measurement circuit, including a Voltmeter with $\pm 1.5~\%$ of reading (500 V or greater) / minimum resolution of 1V, and an Ammeter with $\pm 1.5~\%$ of reading (1 mA or more) / minimum resolution of 1 μ A. In addition, it is also equipped with an Auto range function. The Lower limit judgment accuracy achieves a level of performance equivalent to the Upper limit judgment accuracy that enables to detect for such a poor contact or disconnections of test leads. Moreover, it realizes the fast judgment by the test time of 0.1 second, while reliable testing can be performed, thanks to high-precision, high-resolution, high-speed measurement and the judgment functions.



▲ AC Hipot (Withstanding voltage) test settings display (example)

Supporting the World-wide input voltage



Usable in any country, without changing the input power supply.

Selectable output frequency!

The instrument not rely on the input power environment. Supplying the stable test voltage with 50/60 Hz frequencies.

Reducing the tact time

Reduction of the tact time leads to improve the productivity. However, it has been an issue that reducing the tact time may cause to worsen the measurement accuracy when the test time is faster than the measuring response speed. The TOS5300 series has been achieved to set the test time from 0.1 s.

(Model TOS5301)

6 kV/50 WDC Hipot (Withstanding voltage) test

Capable to perform DC Hipot (Withstanding voltage) test up to 6 kV. (Model TOS5301) Equipped with a stable DC/DC converter with a low-ripple and the load variation of 3 % or less.

Insulation resistance test for 25 V to 1000 V*

The TOS5302 is equipped with an insulation resistance tester. The test voltages can be set from 25 V, 50 V, 100 V, 125 V, 250 V, 500 V and 1000 V. And for setting at 500 V and above, it can perform the insulation resistance test up to $5.00~G\Omega$.

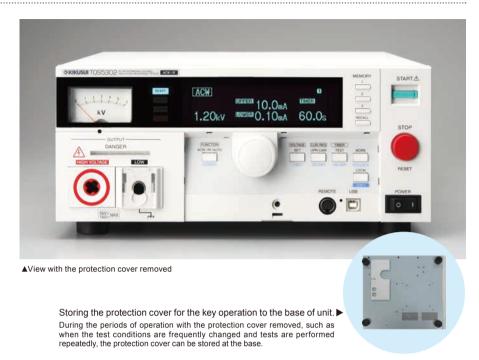
*At 500 V and above, measurements up to 5.00 G Ω are possible.

Protection cover prevents physical operation error in the production site

Prevents a physical operation error by installing the protection cover on the control keys.



In many cases, workers on electronic equipment production lines and inspection lines are not technical experts. Therefore, it is possible that the operators may change setting conditions and make operation errors. In order to prevent from such cases, the TOS5300 is equipped with a key lock function and a protection cover to disable a physical key operation from the front panel.



New design of output terminal improves safety and functionality

Eliminates the projected components from the front panel. The new design of LOW terminal.

In consideration of safety for the operator and the environment, the output terminal of HIGH-side has been placed in the most distant location from the control area. The free rotation machanisim protects from twisting (or breaking) of the cable. Also, with having the lock function for the LOW terminal on the main unit, the metal plate is no longer attached to the test lead of LOW-side, and it makes to resist damage to the test lead. Because of elimination of these projected components, the TOS5300 can avoid from unexpected accidents such as when the unit travels to other location. And also when the test lead is snagged on something, or unexpected stress is applied on the test lead, the High (High-voltage) test lead is designed to disconnect easily, but the Low (ground) test lead is designed to resist disconnection.

In order to prevent the insertion error, the color coding of the cable are classified to HIGH (red) and LOW (black) , and the plug shape of terminal are also different design.



▲ Flat surface design of the HIGH terminal with free rotation mechanism, and the LOW terminal with lock function

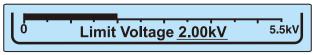


It could be a cause of defect if the cable are incorrectly wired for the HIGH (High-voltage) side and LOW (Low-voltage) side. In order to prevent the insertion error, the protection plate is attached to the Low-voltage (Black) test lead.

Monitoring the output voltage and protection when applying an overvoltage by the operation error

LIMIT VOLTAGE function

Prevents the user from setting a test voltage that exceeds the preset voltage.



▲LIMIT VOLTAGE setting (example)

Monitoring output voltage function

If the output voltage exceeds the setting voltage of $(\pm 350 \text{ V})$, it turns off the output and the system switches to PROTECTION mode.

In order to handle kilo's of high voltage when the Hipot (Withstand voltage) and insulation tests are conducted, there are number of safety measures are required to take place. Having with these functions improve, the operational safety and the protection for the EUT.

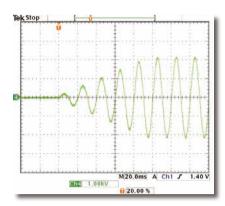
Rise time / Fall time control function

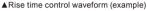
Rise time control function

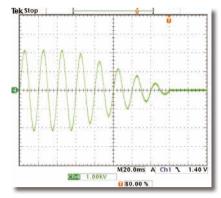
The Rise time control function enables you to increases the test voltage gradually to reach the setting voltage while the AC Hipot (Withstanding voltage) test is conducted. The voltage rise time can be set from 0.1 s to 10.0 s at a resolution of 0.1 s.

Fall time control function

The Fall time control function enables you to decrease the test voltage gradually when the PASS judgment is made at the AC Hipot (Withstanding voltage) test. The voltage fall time is fixed at 0.1 s. (OFF is also selectable).





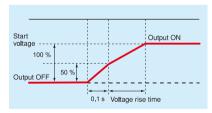


▲Fall time control waveform (example)



The rise time control function is to prevent the EUT (test object) from being exposed to stress that exceeds the required amount. The Hipot (Withstanding voltage) test is conducted to verify the safety performance of the EUT and which test voltage for Hipot (Withstanding voltage) test is applied approximately five to ten times greater than the voltage that handles by the EUT. If a high voltage is applied rapidly with no rise time, the transitional large voltage (current) will be occurred, and it may cause a damage to the EUT. If, as a result of the test, the EUT is suffered of the insulation (dielectric) breakdown, the failure of defect can easily be identified without any problem. However, when breakage is occurred partially, it becomes hard to identify the problem. In other words there are cases in which "at a glance, a Hipot (Withstanding voltage) test appears to have been successfully passed, however, the fact is found that the insulation performance has degraded." In such cases there is a potential risk of danger that the insulation failure will occur after the EUT has been released into the market as a commercial product. The result of testing performed to confirm safety may cause the loss of product's safety. For this reason, safety standards stipulate the procedure of Hipot (Withstanding voltage) test, and the test voltage must be gradually increased to the specified voltage when the test is performed. The rise time control function adopted in the in the TOS5300 Series can set the voltage rise time from 0.1

s to 10.0 s (at a resolution of 0.1 s) and also it is capable to set the 50 % (fixed) of the applied test voltage. In addition, the fall time control function enables to decrease the test voltage gradually after the completion of a PASS judgement. The voltage fall time is fixed at 0.1 s (OFF is also selectable).



▲Start voltage can be set at 50 % of the test voltage.

Examples of Safety Standards (Routine Tests)

●IEC60950/J60950

- Information processing equipment

The test voltage applied to the insulation part should be increased gradually from zero to the specified voltage, then hold at the specified voltage for 60 seconds.

■IEC60335/J0335 - Home appliances

The initial test voltage should be applied less than half of the specified voltage, then gradually increase to the specified voltage.

■IEC60065/J0065 - Audio/video

The initial test voltage should be applied less than half of the specified voltage, then rapidly increase to the specified voltage and hold for 1 minute.

●IEC61010/JIS C 1010

-Measurement equipments

Avoids any detectable transient phenomenon, the test voltage should be increased gradually to the specified voltage within 5 seconds, then hold at the specified voltage for 5 seconds.







Q.What is a Hipot (Withstanding voltage) test?

A. Withstanding test also called a dielectric strength test or Hipot test, a withstanding voltage test is intended to verify whether an electrical product or part has sufficient dielectric strength with respect to the voltage being handled.

Q.What is PASS / FAIL criteria?

A. It is considered as "Flectrical breakdown" when the current exceeds the limit value flowing through the insulated section during a test. If "the Electrical breakdown" does not occur, the insulator is determined to have sufficient insulating strength.

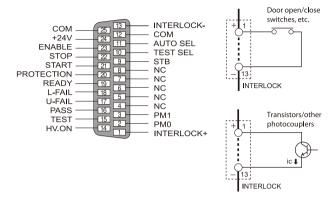
Q.How is the test conducted?

A. Apply the voltage with much higher stress than it would normally be applied to the insulated section for the specified time period.

While testing, it evaluates to verify whether any insulation breakdown has occurred on the insulator.

Interlock feature

The product is equipped with an interlock function that operates together with external devices to interrupt output. To ensure the safe operation of tester, the interlock function activates when the SIGNAL I/O connector pins number 1 and 13 are opened, and when they are short-circuited, the interlock function is released.





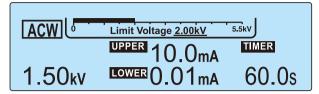
▲The picture shown above indicates the caution sign of " HI VOLTAGE DANGER " with a chain surrounding the test site.

Discharge feature (Model TOS5301 / TOS5302)

Equipped with a forced discharge function that forcibly discharge the electricity which has been charged in the EUT after the completion of DC Hipot (Withstanding voltage) test or insulation resistance test.

Upper limits / Lower limits setting function

It automatically detects connector lead breaks and disconnections of wiring by measuring extremely small amounts of current that flows when voltage is applied to the EUT.



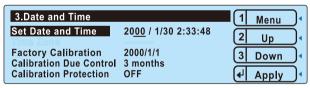
▲Example setting display of Upper limit, Lower limit, and Test time



Normally, even with a good-quality EUT, a certain degree of leakage current flows. If the current value is set at slightly smaller than the specific range of the EUT, it is useful in detecting breaks of the test lead and faulty connections, which enables tests to be performed with even higher reliability. You can perform testing effectively if you set the lower limit value with LOWER ON during Hipot (Withstanding voltage) test, and the upper limit value with UPPER ON during insulation resistance test.

Calibration due notice and Warning function

To assure the traceability of periodic calibration of the product, this function gives a notice of calibration due managed by the built-in real-time clock. Even if the due data has elapsed, it is possible to avoid the oversight of operator with limiting the operation with a display of warning message.



▲Example setting display of Calibration due







Q.What is an insulation resistance test?

A. An insulation resistance test is to measure the resistance value of insulator and verify that whether the insulator has a sufficient performance. It is similar to the Hipot (Withstanding voltage) test that confirms the function or performance of an insulator, and it should be the required conditions to prevent the accidents from an electrical shock and fire.

Q.What is the procedure of testing?

A. In many cases, after moisture is absorbed (or is not absorbed in some cases), 500 volts or other specified value of DC voltage is applied, and the resistance value is measured from the current flowing.

Q.What is the difference between an insulation resistance test and a Hipot test?

A. The Hipot (Withstanding voltage) test detects a faulty insulation whether insulation breakdown occurs. In contrast, the insulation resistance test detects faulty insulation by measuring the resistance value.

Easy setting of test conditions with panel memory feature!

MEMORY

1

2

3

RECALL

panel memory feature!

To set the test conditions such as test voltage, judgment value and test duration, simply press a key and turn the knob on the front panel. The test conditions can be saved in the panel memory (3 sets).

■ Panel memory setting key

(Model TOS5302)

AUTO TEST feature for consecutive testing

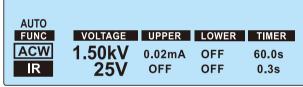
The TOS5302 can perform an AC Hipot (Withstanding voltage) test and an insulation resistance test consecutively.

Either of the following can be conducted:

Insulation resistance test \rightarrow AC Hipot (Withstanding voltage) test, or AC Hipot (Withstanding voltage) test \rightarrow Insulation resistance test.

AUTO				
FUNC	VOLTAGE	UPPER	LOWER	TIMER
IR	25V	OFF	OFF	0.3s
ACW	1.50kV	10.0mA	OFF	60.0s
7.011	IJUKV	IU.UIIIA	OFF	00.05

▲Insulation resistance test → AC withstand-voltage test



▲AC withstand-voltage test → Insulation resistance test

REMOTE connector & USB interface



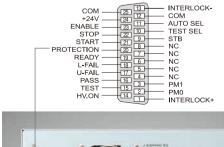
Equipped with the REMOTE connector and USB interface on the front panel are exclusive use for the options.

Easy connection with the PC.

SIGNAL I/O Connector

The rear panel is equipped with SIGNAL I/O that provides functions such as start and stop operation and signal output.

Interlock	PIN No	Signal name	I/O	TOS5300	TOS53	01			TOS5302
The selection signal is latched on the rising edge of the input strobe signal to recall panel memory. The selection of memory is prioritized over TEST SEL and AUTO SEL. Whence over TEST SEL and AUTO SEL. The selection of memory is prioritized over TEST SEL and AUTO SEL. Whence over TEST SEL and AUTO SEL. The selection of memory is prioritized over TEST SEL and AUTO SEL. Whence over TEST SEL and AUTO SEL. The selection of SEL and AUTO SEL. Whence over TEST SEL and AUTO SEL. The selection of SEL and AUTO SEL selection. The selection of SEL and AUTO SEL selection of Single Industry. The selection signal Industry. The selection of Single Industry. The selection of Single Industry. The selection signal Industry. The selecti	1	INTERLOCK+	ı	off, and the TOS5300 S The resistance betwee	eries is s n the tv	witch vo ter	hed to mina	o Prot Is is 1	tection mode.Open: .2 kΩ or greater. Short:
PMI	2	PM0	1			PM0	PM1	Calle	d Panel Memory Number
recal panel memory. *The selection of memory is prioritized over TEST SEL and AUTO SEL. ## NC	3	PM1	ı			Н	Н	Mem	ory 1
A NC					. signar to	<u>⊢</u>		_	,
Solution of Autorian Selection of Autorian Selection of Autorian Selection of Selection Selection Selection of Autorian Selection S					rioritized		_		
NC - - -	4	NC	_			_			
7 NC	5	NC	_			_			_
STB	6	NC	_			_			
Panel memory's strobe signal input terminal Single/Independent test's selection signal/AUTO Test's sequence selection signal Selection of single selection signal L: ACW H: DCW Selection of AUTO test with AUTO SEL L: ACW H: DCW Selection of AUTO test with AUTO SEL L: ACW H: DCW Selection of AUTO test/single test L: ACW H: DCW Selection of AUTO test/single test L: Single test H: AUTO test Open: Terminal-to-terminal resistance is 1.2 kΩ or less Open: Terminal-to-terminal resistance is 1.2 kΩ or less ON during test and while voltage remains between output terminals ON during test, while voltage remains between output terminals ON during test (except when voltage is rising or falling) ON during test (except when PASS has been determined (PASS HOLD time) On time of the continuously ON when PASS HOLD time is set for HOLD On time of tested, and UPPER FAIL is determined On the continuously ON when value under acceptable maximum is detected, and UPPER FAIL is determined On the continuously ON while protection function is activated (PROTECTION ON) ON while protection function is activated (PROTECTION ON) ON while protection function is activated (PROTECTION ON) Start signal input terminal Start signal in	7	NC	_			_			
Single/Independent test's selection signal/AUTO Test's sequence selection Test's sequence selection Singla Selection of single test with AUTO SEL L: ACW H: DCW Selection of AUTO Selection	- 8	NC	_			_			
Selection signal/AUTO Test's sequence selection signal Selection of single Selection of AUTO Selection Selection of AUTO Selection Select	9	STB	1	Panel men	nory's st	robe	signa	al inp	ut terminal
11 AUTO SEL I NA NA Lest/single test L: Single test L: Single test L: Single test H: AUTO test 12 COM — Circuit's common terminal When + terminal and - terminal are opened, output is interrupted and the system shifts to the Protection status. Open: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal-to-terminal resistance is 1.2 kΩ or less ON during test and while voltage remains between output terminals ON during test except when voltage is rising or falling) 15 TEST O ON during test (except when voltage is rising or falling) 16 PASS O ON for at least 0.2 sec. when PASS has been determined (PASS HOLD time) Continuously ON when PASS HOLD time is set for HOLD 17 U-FAIL O Continuously ON when value over acceptable maximum is detected, and UPPER FAIL is determined 18 L-FAIL O Continuously ON when value under acceptable minimum is detected, and LOWER FAIL is determined 19 READY O ON during standby (READY status) 20 PROTECTION O ON while protection function is activated (PROTECTION ON) 21 START I Start signal input terminal 22 STOP I Stop signal input terminal 23 ENABLE I Start signal's ENABLE signal input terminal: Shifts to the Protection status when the ENABLE signal changes	10	TEST SEL	ı	ACW/DCW selection signal L: ACW H: DCW				Test's sequence selection signal Selection of single test with AUTO SEL L: ACW; H: DCW Selection of AUTO test with AUTO SEL L: ACW→ IR	
When + terminal and - terminal are opened, output is interrupted and the system shifts to the Protection status. Open: Terminal-to-terminal resistance is 1.2 kΩ or more Short: Terminal set 1.2 kΩ or more Short: Terminal sh	11	AUTO SEL	1	NA NA test/single test L: Single test					test/single test L: Single test
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23 ENABLE I Shifts to the Protection status when the ENABLE signal changes 24 +24V — +24V internal power supply output terminal: Maximum output current 100 mA	22	STOP	I	Stop signal input term	inal				
	23	ENABLE	ı						BLE signal changes
25 COM — Circuit's common terminal	24	+24V	_	+24V internal power supp	ly output	term	inal: N	laximu	um output current 100 mA
	25	СОМ	_	Circuit's common term	ninal				





Unless specified otherwise, the specifications are for the following settings and conditions.

- The warm-up time is 30 minutes.
 TYP:These are typical values. These values do not guarantee the performance of the product.
 rdng: Indicates the readout value.
- set: Indicates a setting.
- f.s: Indicates full scale.

■ Specifications –Withstanding voltage tester

				TOS5300	то	DS5301	TOS530)2			
	Output range				0.05 k\	√ to 5.00 kV					
		Accuracy			±(2 % of set + 20 V) v	vhen no load is connected					
		Setting range		0.00 kV to 5.50 kV							
		Resolution	10 V steps								
	Max. rated ou			500 VA (5 kV/100 mA)							
	Max. rated vo		5 kV								
	Max. rated cu					it voltage is 0.5 kV or grea	ter)				
	Transformer i						torj				
AC output section		e waveform *2		500 VA							
	Output voitag	Distortion		Sine							
	Frequency	Distortion		If the output voltage is 0.5 kV or more: 3 % or less (when no load or a pure resistive load is connect 50 Hz or 60 Hz							
	requericy	A									
	\/=\k========	Accuracy		40.0/		during voltage rise time)	11				
	Voltage regul					om maximum rated load to					
	Input voltage					cted; power supply voltage					
Short-circuit				200	-	utput voltage is 1.0 kV or g	greater)				
	Output metho	α				switching					
	Output range					V to 6.00 kV					
		Accuracy				of set + 20 V) ad is connected					
		Setting range	-			v to 6.20 kV					
		Resolution	_			V steps					
	Max. rated ou					5 kV/10 mA)					
	Max. rated vo		-			6 kV					
OC output	Max. rated cu		-			10 mA					
section	Max. Taleu Cu	5 kV when no	-	_		IO IIIA	_				
	Ripple (TYP)	load is connected	_			0 Vp-p					
		Max. rated load	-			00 Vp-p					
	Voltage regul	ation				changing from maximum ad to no load)					
	Short-circuit	current (TYP)			40 mA (when ge	neration 6 kV output)					
	Discharge fea	ature			_	e after test completion esistance: 125 kΩ)					
Start Voltage	!			The voltage at the	e start of withstanding vol	tage tests can be set to 50	0% of the test voltage.				
Limit Voltage				The test voltage	upper limit can be set .	AC: 0.00 kV to 5.50 kV, DO	C: 0.00 kV to 6.20 kV				
Output voltag	ge monitor feat	ıre	If output voltage exceeds the specified value + 350 V or is lower than the specified value - 350 V, output is turned off, and protective features are activated.								
		Scale			6 kV	AC/DC f.s					
	Analog	Accuracy			±	5 % f.s					
		Indication			Mean-value re	esponse/rms scale					
Voltmeter		Measurement range			0.000 kV to	6.500 kV AC/DC					
VOILITICICI		Display				□□□ kV					
	Digital	Accuracy		V		20 V); V ≥ 500 V: ±1.5 %	of rdna				
		Response *3				nse rms display Can be s					
		Hold feature			·		r FAIL judgment is cleared.				
		Measurement		7 titel a test lo lillionea,		mA to 110 mA	Trac juagment is sicured.				
		range	AC:	0.00 mA to 110 mA		mA to 11 mA	AC: 0.00 mA to	110 mA			
			i = measured c	urrent							
		Dieplay		i < 1 mA	1 mA ≤ i < 10 mA	10 mA ≤ i < 100 mA	100 mA ≤ i				
Ammeter	Digital	Display		□□□ μА	□.□□□ mA	□□.□□ mA	□□□.□ mA				
Ammeter L		Accuracy *4		1.00 ı	mA ≤ i: ±(1.5 % of rdng); i	< 1.00 mA: ±(1.5 % of rdng	g + 30 µA)				
		Response *3		True	e rms/ Mean-value respon	nse rms display Can be s	witched				

■ Specifications –Withstanding voltage tester

					TOS5300	TOS5301		то	S5302		
			Judgi	ment	Juda	ment method	Display	SIGNAL I/O			
			UPP FA	PER	If a current that is greater than the output is turned off, and ar an UPPER FAIL judgment occ	or equal to the upper limit is detected, n UPPER the output is turned off, and turs. During the voltage rise time (Rise age tests, an UPPER FAIL judgment	FAIL LED lights	ON	Generates a U-FAIL signal		
	Judgment metl		LOW FA	VER	the output is turned off, and a judgment is not performed dur	equal to the lower limit is detected, LOWER FAIL judgment occurs. This ring voltage rise time (Rise Time) of all Ill time (Fall Time) of AC withstanding	FAIL LED lights UNDER is displayed on the screen	ON	Generates a L-FAIL signal		
Judgment feature			PAS		If the specified time elapses w turned off, and a PASS judgmo	rithout any problems, the output is ent occurs.	PASS LED lights	ON	Generates a PASS signal		
				PPER AIL an ASS ju	FAIL and LOWER FAIL signal of PASS buzzer volume levels	at the buzzer sounds for is fixed to 0.2	TOS5300 Series rec				
	Upper limit set	ting		AC:	0.01 mA to 110 mA	AC: 0.01 mA to 110 mA DC: 0.01 mA to 11 mA	AC: 0.01 mA to 110 mA				
	Lower limit set	ting	A	.C: 0.0	1 mA to 110 mA / OFF AC: 0.01 mA to 110 mA / OFF DC: 0.01 mA to 11 mA / OFF AC: 0.01 mA to			to 110 mA / OFF			
	Judgment accu	uracy *4			1.00 mA	$x \le i$: ±(1.5 % of set), i < 1.00 mA: ±(1.5 %	% of set + 30 μA)				
	Current detect	ion method			Calculates the current's true	rms value or mean-value and compare	es this value with the	reference	e value		
	Calibration				Calibrate	ed with the rms of a sine wave using a p	oure resistive load				
	Voltage rise tin	ne				0.1 s to 10.0 s					
		Resolution	0.1 s								
	Voltage fall tim	e	0.1 s / OFF (only enabled when a PASS judgment occurs)								
Γime	Test time					0.1 s to 999 s, can be turned off (TIME	R OFF)				
		Resolution				0.1 s to 99.9 s: 0.1 s. 100 s to 999 s	: 1 s.				
	Accuracy				±(100 ppm + 20 ms) AC: Excluding Fall Time DC: Rise Time Add ±50 ms at 1 kV or more, Add ±100 ms at less than 1kV.						

*1. Regarding the output time limits:

Taking size, weight, and cost into consideration, the heat dissipation capability of the voltage generator that is used for withstanding voltage tests has been designed to be one half that of the rated output. Use the TOS5300 Series within the following limits. If you use the product in a manner that exceeds these limits, the output section may overheat, and the internal protection circuits may be activated. If this happens, stop testing, and wait until the TOS5300 Series returns to its normal temperature.

Ambient temperature	Upper	·limit	Pause time	Output time	
	AC	50 mA < $i \le 110$ mA Greater than or equal to the output time		30 min. max.	
t≤40 °C	AC	i ≤ 50 mA Not necessary		Continuous output possible	
(≤40 C	DC	5 mA < i ≤ 11 mA	Greater than or equal to the output time	1 min. max.	
		i ≤ 5 mA	Greater than or equal to the wait time (WAIT TIME)	Continuous output possible	

(Output time = voltage rise time + test time + voltage fall time)

*2. Regarding the test voltage waveform:

Waveform distortions may occur if an DUT whose capacitance is dependent on voltage (for example, an DUT that consists of ceramic capacitors) is connected as the load. However, if the test voltage is 1.5 kV, the effect of a capacitance of 1000 pF or less can be ignored. Because the product's high-voltage power supply uses the PWM switching method, if the test voltage is 500 V or less, the switching and spike noise proportions are large. The lower the test voltage, the greater the waveform is distorted.

*3. For both True rms and Mean-value response, 50 ms or above response time is required to satisfy the measurement accuracy.

*4. Regarding ammeter and judgment accuracy:

During AC withstanding voltage tests, current also flows in the stray capacitance of items such as the measurement leads and jigs. This current that flows in the stray capacitances is added to the current that flows in the DUT, and the sum of these currents is measured. Especially if you want to perform judgments with high sensitivity and accuracy, it is necessary to consider methods to limit the current that flows in these stray capacitances, such as by adding upper and lower limits.

Output voltage	1 kV	2 kV	3 kV	4 kV	5 kV
When using 350 mm long test leads that are suspended in air (TYP)	2 μΑ	4 μΑ	6 μΑ	8 μΑ	10 μΑ
When using the accessory, high test lead TL31-TOS (TYP)	16 µA	32 µA	48 µA	64 μΑ	80 μΑ

■ Specifications –Insulation resistance test section

								TOS5	302					
	Output voltag	e		25 V, 50 V, 100 V, 125 V, 250 V, 500 V, 1000 VDC (negative)										
		Accuracy	-0 %, +5 %											
	Max. rated loa	ad						1 W (-1000 V	DC / 1 mA)					
	Max. rated cu	irrent						1 m.	A					
	Ripple	1000 V when no load is connected		2 Vp-p or less										
Output section	Кірріе	Max. rated load						10 Vp-p	or less					
	Voltage regul	ation				1 %	or less (when c	hanging from r	maximum rate	d load	d to no lo	ad)		
	Short-circuit	current						12 mA o	r less					
	Discharge fea	ature				Forced dis	scharge after te	st completion (discharge res	istan	ce: appro	x. 25 kΩ)		
	Limit voltage				Th	e test voltag	e upper limit ca	in be set : 25 V	, 50 V, 100 V,	125 V	/, 250 V, 5	500 V, 1000 V		
	Output voltag	e monitor feature	If output	voltage e	exceeds "10 %	6 of set + 10	V" or is lower th	nan "-(10 % of	set + 10 V)," o	utput	is turned	off, and prote	ctive feat	ures are activat
		Scale						6 kV AC/	DC f.s					
	Analog	Accuracy						±5 %	f.s					
		Indication					Me	an-value respo	onse/rms scal	e				
/olt-		Measurement range						0 V to -1:	200 V					
neter					Magauradi	volto a o	V = 40	20.1/	100 \/ < \/	- 100	00.1/	1000	11/21/	
	Digital	Display			Measured v Displa		V < 10		100 V ≤ V		00 V		0 V ≤ V □□ V	
		Accuracy						± (1 % of rd	ng + 1 V)					
		25 V						R ≤ 25 MΩ / ± IΩ < R ≤ 125 M			jits)			
		23 V					125 N	IΩ < R ≤ 250 M	Ω / ±10 % of r	dng				
Resis- tance meter		50 V					50 M	$R \le 50 M\Omega / \pm 10 < R \le 250 M$ $10 < R \le 250 M$ $10 < R \le 500 M$	Ω / ±5 % of ro	ng	jits)			
	Measurement	100 V		250 MΩ < R ≤ 500 MΩ / ±10 % of rdng 0.100 MΩ ≤ R ≤ 100 MΩ / ±2 % of rdng 100 MΩ < R ≤ 500 MΩ / ±5 % of rdng 500 MΩ < R ≤ 1 GΩ / ±10 % of rdng										
	range / measurement accuracy *5 *6	125 V		0.125 MΩ ≤ R ≤ 125 MΩ / ±2 % of rdng 125 MΩ < R ≤ 625 MΩ / ±5 % of rdng 625 MΩ < R ≤ 1.25 GΩ / ±10 % of rdng										
	5 6	250 V		0.250 MΩ ≤ R ≤ 250 MΩ / \pm 2 % of rdng 250 MΩ < R ≤ 1.25 GΩ / \pm 5 % of rdng 1.25 GΩ < R ≤ 2.5 GΩ / \pm 10 % of rdng										
		500 V		0.50 M Ω \leq R \leq 500 M Ω / \pm 2 % of rdng 500 M Ω $<$ R \leq 2.5 G Ω / \pm 5 % of rdng 2.5 G Ω $<$ R \leq 5 G Ω / \pm 10 % of rdng										
		1000 V		1 M Ω \leq R $<$ 1 G Ω / ± 2 % of rdng 1 G Ω \leq R \leq 5 G Ω / ± 5 % of rdng										
	Display *6		25	kΩ ≤ R <	< 1.00 MΩ		R < 10.0 MΩ	10.0 MΩ ≤ R		100.0) MΩ ≤ R			≤ R ≤ 9.99 GΩ
11-1-164		-												
Hold feat					Arter a	test is tinisn	ed, the measur					igment is clea	irea.	
Surrent o	detection respo	nse speed					Can be switch	ed between th	ree levels: Fas	St, Mic	a, Slow			
			Juc	lgment			Judgment m	ethod			D	isplay	Buzzer	SIGNAL I/O
			UPP	ER FAIL	the output is	turned off, a	eater than or equand an UPPER	FAIL judgment	occurs. This	ted,	OVER i	ED lights; s displayed e screen	ON	Generates a U-FAIL signal
			LOW	If a resistance that is less than or equal to the lower limit is detected or if FAIL LED lights;						ON	Generates a L-FAIL signal			
lood a	operation	thod and judgment	P	ASS		ed time elap	ses without any		output is turr	ned		LED lights	ON	Generates a PASS signal
Judg- ment feature			If PASS HOLD is enabled, the PASS signal is generated continuously until the TOS5300 Series receives a STOP signal. The UPPER FAIL and LOWER FAIL signals are generated continuously until the TOS5300 Series receives a STOP signal. The FAIL and PASS buzzer volume levels can be changed. For PASS judgments, the length of time that the buzzer sounds for is fixed to 0.2 seconds. Even if PASS HOLD is enabled, the buzzer turns off after 0.2 seconds.											
	Upper limit se	etting range	0.03 ΜΩ	to 5.00	GΩ									
	Lower limit se	etting range	0.03 ΜΩ	to 5.00 (GΩ									
Measurement accuracy + 2 digits Judgment accuracy (the same for UPPER and LOWER) Measurement accuracy + 2 digits Humidity: 20 %rh to 70 %rh (no condensation). No interference caused by wobb For judgments of 200 nA or less, a test time of at least 1.0 seconds is necessary If the current detection response speed is set to Mid, a test time of at least 0.3 s If the current detection response speed is set to Slow, a test time of at least 0.5					cessary.	s is ne	ecessary.	•						
					ection respons	se speed is	set to Slow, a te	st time of at lea	ast 0.5 secon	ds is r	necessarı	/.		
	LOWER)	ime	If the cui	rrent dete	ection respons	se speed is s	set to Slow, a te	est time of at lea	ast 0.5 secon	ds is r	necessary	/.		
	LOWER) Voltage rise t	ime	If the cui	rrent dete	-			est time of at lea	ast 0.5 secon	ds is r	necessary	/.		
Time	LOWER)	Resolution	If the cur 10 ms (7 0.1 s to 9	rrent dete TYP) 999 s, ca	n be turned of	ff (TIMER O		est time of at lea	ast 0.5 secon	ds is r	necessar	/.		

^{*5.} Humidity: 20 %rh to 70 %rh (no condensation). No bends in the test leads. *6. R = measured insulation resistance

■ Specifications – Other features / Interfaces

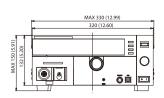
		TOS5300	TOS5301	TOS5302				
Double a	ction feature	in 0.5 seconds of releasing the STOP switch.						
Length of	time to maintain a PASS judgment resul	t You can set the length of time t	o maintain a PASS judgment: 50 ms, 100 ms	, 200 ms, 1 s, 2 s,5 s, or HOLD.				
Momenta	ry feature	Tests are	e only executed while the START switch is he	eld down.				
Fail mode	e feature	This feature enables you to prevent remotely transmitted stop signals from clearing FAIL judgments and PROTECTION modes.						
Timer fea	iture	This fe	ature finishes tests when the specified time e	elapses.				
Output vo	oltage monitor feature	, ,	e exceeds "setting + 350 V" or is lower than " ches to PROTECTION mode, output is turne	•				
Memory		Up to th	nree sets of test conditions can be saved to n	nemory.				
Key lock		Loc	cks panel key operations (settings and chang	es).				
Protective	e features	, ,	e TOS5300 Series switches to the PROTEC ops testing. A message is displayed on the s					
l i	Interlock Protection		An interlock signal has been detected.					
	Power Supply Protection		An error was detected in the power supply.					
	Volt Error Protection	While monitoring the output voltage, a voltage outside of the rated limits was detected. AC or DC withstanding voltage tests: ±350 V Insulation resistance test: ±(10 % of set + 10 V)						
	Over Load Protection	During a withstanding voltage test, a value that is greater than or equal to the output limit power was specified. AC withstanding voltage test: 550 VA. DC withstanding voltage test: 55 VA.						
	Over Heat Protection	The internal temperature of the TOS5300 Series became too high.						
	Over Rating Protection	During a withstanding voltage test, the	e output current was generated for a length o	f time that exceeds the regulated time.				
	Calibration Protection		The specified calibration period has elapsed					
	Remote Protection	A connection to or dis	connection from the front-panel REMOTE co	nnector was detected.				
	SIGNAL I/O Protection	The rear-par	nel SIGNAL I/O connector's ENABLE signal h	nas changed.				
l	USB Protection	The USB connector has been discon	nected while the TOS5300 Series was being	controlled through the USB interface.				
System c	lock	Set in the fo	ollowing format: year/month/day hour/minute	es/seconds.				
	Calibration date		Set when the TOS5300 Series is calibrated.					
	Calibration period setting	Sets	the period before the next calibration is nece	ssary.				
	Notification of when the calibration	Sets the operation	that is performed when the specified calibrat	ion period elapses.				
	period elapses	When the TOS5300 Series turns on	, it can display a notification or switch to the	protection mode and disable testing.				
	USB		USB Specification 2.0					
Interface	s REMOTE	By connecting an optional device	Front-panel 9-pin MINI DIN connector. e to this connector, you can control the startir	ng and stopping of tests remotely.				
	SIGNAL I/O	Rear-panel D-sub 25-pin connector						

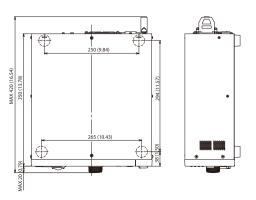
■ Specifications –General

			TOS5300	TOS5301	TOS5302			
Display				VFD: 256 × 64 dots + 4 status indicators				
Backup b	attery life			3 years (at 25 °C or 77 °F)				
	Installation loca	tion		Indoors, at a height of up to 2000 m				
	Spec guarante-	Temperature		5 °C to 35 °C (41 °F to 95 °F)				
Fautirea	ed range	Humidity		20 %rh to 80 %rh (no condensation)				
Environ- ment	Operating	Temperature		0 °C to 40 °C (32 °F to 104 °F)				
IIICIII	range	Humidity		20 %rh to 80 %rh (no condensation)				
	Storage range	Temperature		-20 °C to 70 °C (-4 °F to 158 °F)				
	Storage range	Humidity		90 %rh or less (no condensation)				
	Nominal voltage ra	nge(allowable voltage range)		100 VAC to 240 VAC (90 VAC to 250 VAC)				
Power	Power con- When	no load is connected (READY)		100 VA or less				
supply	sumption Whe	n rated load isconnected	800 VA max.					
	Allowable frequ	ency range	47 Hz to 63 Hz					
Insulation	resistance (betwee	en AC LINE and the chassis)		30 MΩ or more (500 VDC)				
Withstand	ing voltage (between	en AC LINE and the chassis)	1400 VAC, 2 seconds (Routine test) / 1500 VAC, 1 minutes (Type test)					
Earth co	ntinuity		25 AAC, 0.1 Ω or less					
Safety *7			·	h the requirements of the following directive a				
- Curoty			<u> </u>	ive 2014/35/EU*8, EN 61010-1 (Class I*11, Po				
			·	n the requirements of the following directive a				
				26-1 (Class A*9), EN 55011 (Class A*9, Group				
Electrom	agnetic compatit	oility (EMC) *7 *8		ne maximum length of all cabling and wiring c				
			Shielded cables are being used when using the SIGNAL I/O. The high-voltage test lead TL31-TOS is being used.					
Dimension			Electi	rical discharges are not occurring outside the	DU1.			
Dimensio	ons		441 (00 0 11)	See "Outline drawing."	4.4.4.400.0.11.3			
Weight			Approx. 14 kg (30.9 lb.)	Approx. 15 kg (33.1 lb.)	Approx. 14 kg (30.9 lb.)			
Accesso	ries		, ,	(TL31-TOS): 1set (1 red wire and 1 black wire				
			D-sub 25-pin plug: 1set; assembly type / High-voltage warning sticker: 1pc. / User's manual: 1pc. / CD-R: 1pc. *9					

Outline drawing

Unit: mm (inch)





- *7 Does not apply to specially ordered or modified TOS5300s.
- 8 Limited to products that have the CE mark on their panels. Not be in compliance with EMC limits unless the ferrite core is attached on the cable for connection of J1 connector.
- 9 This is a Class A equipment. The TOS5300 is intended for use in an industrial environment. This product may cause interference if used inresidential areas. Such use must be avoided unless the user takes special measures to reduce electromagnetic emissions to prevent interference to the reception of radio and television broadcasts.
- *10 This is a Group 1 equipment. The TOS5300 does not generate and/or use intentionally radio-frequency energy, in the from of electromagnetic radiation, inductive and/or capacitive coupling, for the treatment of material or incredition (angle) in purpose.
- inspection/analysis purpose.

 **11 This is a Class I equipment. Be sure to ground the TOS5300's protective conductor terminal. The safety of this product is only guaranteed when the product is properly grounded.
- product is properly grounded.

 *12 Pollution is addition of foreign matter (solid, liquid or gaseous) that may produce a reduction of dielectric strength or surface resistivity. Pollution Degree 2 assumes that only non-conductive pollution will occur except for an occasional temporary conductivity caused by condensation.

Ordering information

Product Name	Model	Remarks
AC Hipot (Withstanding voltage) & Insulation Resistance tester (ACW/IR)	TOS5302	Hipot (Withstanding voltage) test: AC 5 kV/100 mA Insulation Resistance test: 25V - 1000V
AC/DC Hipot (Withstanding voltage) tester (ACW/DCW)	TOS5301	Hipot (Withstanding voltage) test: AC 5 kV/100 mA, DC 6 kV/50 W
AC Hipot (Withstanding voltage) tester (ACW)	TOS5300	Hipot (Withstanding voltage) test: AC 5 kV/100 mA

Options

Test Lead

TL31-TOS (Max.AC5 kV/1.5 m)

*Standardly attached to main unit in TOS5300 Series.

TL32-TOS (Max.AC5 kV/3 m)



Test Probe

HP01A-TOS* (Max.AC4 kV-DC5 kV/1.8 m) ■HP02A-TOS* (Max.AC4 kV•DC5 kV/3.5 m)



*The remote control conversion cable [DD-5P/9P] is required when used with TOS5300 Series

Remote Control Box

RC01-TOS* (For single-handed operation/1.5 m)

RC02-TOS* (For two-handed operation/1.5 m)



*The remote control conversion cable [DD-5P/9P] is required when used with TOS5300 Series.

DIN Cable

DD-5P/9P conversion adapter (DIN - MIni DIN)



Warning Light Unit

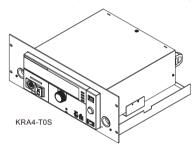
PL02-TOS (for DC24 V)



Rack Mount Adapter

KRA4-TOS (inch rack for EIA standards)

KRA200-TOS (millimeter rack for JIS standards)



Electrical Safety Multi-analyzer

TOS9300 Series



Ground Bond Tester

TOS6210 / 6200A



Insulation Tester

OS7200



Leakage Current Tester

TOS3200



Hipot Tester

OS5200









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